

#2
3-22-01



100-00017 (MP0042)

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
YAT-TUNG LAM) : Examiner: Not Yet Assigned
Application No.: 09/678,728) : Group Art Unit: 2121
Filed: October 4, 2000) :
For: MOVABLE TAP FINITE) :
IMPULSE RESPONSE FILTER : March 16, 2001

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Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

In compliance with the duty of disclosure under
37 C.F.R. § 1.56 and in accordance with the practice under
37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is
directed to the documents listed on the enclosed Form
PTO-1449. Copies of the listed documents are also enclosed.

CONCLUSION

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be returned indicating that such information has been considered.

Applicants' undersigned agent may be reached in our Washington, D.C. office by telephone at (202) 625-3500. All correspondence should be directed to our address given below.

Respectfully submitted,

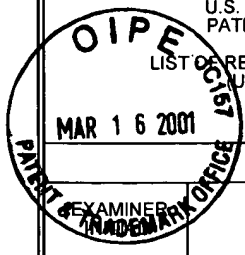
James C. Diomoda

Agent for Applicants

Registration No. 44,727

Patent Administrator
KATTEN MUCHIN ZAVIS
525 West Monroe Street, Suite 1600
Chicago, Illinois 60661-3693
Facsimile: (312) 902-1061

#2



FORM PTO 1449 (modified)	ATTY DOCKET NO. 13200 - 00017 (MP)	APPLICATION NO. 09/678,728 3-22-01
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		
APPLICANT YAT-TUNG LAM		
LIST OF REFERENCES CITED BY APPLICANT(S) (Use several sheets if necessary)		FILING DATE October 4, 2000
		GROUP 2121

U.S. PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
	6,035,320	3/7/00	Kiriaki et al.	708	819		
	5,777,910	7/7/98	Lu	364	724.2		
	5,535,150	7/9/96	Chiang	364	724.19		

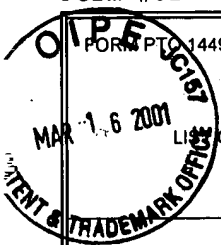
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FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT	
	WO 46867	9/16/99	PCT				

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)	
	"An Adaptive Multiple Echo Canceller for Slowly Time Varying Echo Paths", Yip and Etter, IEEE Transactions on Communications, October 1990
	"Digital Signal Processing", Alan V. Oppenheim et al., pp. 155-163
	"A 100 MHz Output Rate Analog-to-Digital Interface for PRML Magnetic-Disk Read Channels in 1.2 um CMOS", Gregory T. Uehara and Paul R. Gray, ISSCC94/Session 17/Disk-Drive Electronics/Paper FA 17.3, 1994 IEEE International Solid-State Circuits Conference, pp. 280-281
	"72 Mb/s PRML Disk-Drive Chann I Chip with an Analog Sampled Data Signal Processor", Richard G. Yamasaki t al., ISSCC94/Session 17/Disk-Drive Electronics/Paper FA 17.2, 1994 IEEE International Solid State Circuits Conference, pp. 278-279

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



FORM PTO 1449 (modified)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICELIST OF REFERENCES CITED BY APPLICANT(S)
(Use several sheets if necessary)

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APPLICANT YAT-TUNG LAM

FILING DATE October 4, 2000

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

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FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

		"A Discrete-Time Analog Signal Processor for Disk Read Channels", Ramon Gomez et al., ISSCC93/Session 13/Hard Disk and Tape Drives/Paper FA 13.1, 1993 ISSCC Slide Supplement, pp. 162-163 and 279-280
		"A 50 MHz 70 mW 8-Tap Adaptive Equalizer / Viterbi Sequence Detector in 1.2 um CMOS", Gregory T. Uehara et al., 1994 IEEE Custom Integrated Circuits Conference, pp. 51-54

EXAMINER

DATE CONSIDERED

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